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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/805,933	03/22/2004	Yu-Lung Yu	JCLA10375-D	1027
23900	7590	05/18/2005		EXAMINER
J C PATENTS, INC. 4 VENTURE, SUITE 250 IRVINE, CA 92618				LUU, CHUONG A
			ART UNIT	PAPER NUMBER
			2818	

DATE MAILED: 05/18/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

EJC

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	10/805,933	YU ET AL.
	<b>Examiner</b> Chuong A. Luu	<b>Art Unit</b> 2818

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

**A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 03 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.**

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) Responsive to communication(s) filed on 23 February 2005.
- 2a) This action is **FINAL**.                    2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) Claim(s) 5-6 and 8 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) \_\_\_\_\_ is/are allowed.
- 6) Claim(s) 5,6 and 8 is/are rejected.
- 7) Claim(s) \_\_\_\_\_ is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. 10/447,520.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | Paper No(s)/Mail Date. _____  |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
|  | 6) <input type="checkbox"/> Other: _____                                    |

## **DETAILED ACTION**

### ***Response to Arguments***

Applicant's arguments with respect to claims 5-6 and 8 have been considered but are moot in view of the new ground(s) of rejection.

The indicated allowability of claim 7 is withdrawn in view of the newly discovered reference(s) to 6,674,159 B1. Rejections based on the newly cited reference(s) follow.

## **PRIOR ART REJECTION**

### **Statutory Basis**

#### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

### **The Rejections**

Claims 5 and 8 are rejected under 35 U.S.C. 102(e) as being anticipated by Peterson et al. (U.S. 6,674,159 B1).

Peterson discloses a microelectronic device with

(5) a chip with an active surface having at least a flip-chip bonding pad and at least a test pad thereon, wherein the test pad is positioned on the active surface and is electrically connected to the flip-chip bonding pad, and the chip furthermore having at least a trace line on the active surface for connecting the test pad to the flip-chip bonding pad;

a passivation layer formed over the active surface, wherein the passivation layer exposes the flip chip bonding pad, and covers the trace line (see Figures 10C and 10D);

(8) wherein the chip furthermore comprises a bump attached to the flip-chip bonding pad (see Figures 10C and 10D).

## PRIOR ART REJECTION

### Statutory Basis

#### *Claim Rejections - 35 USC § 103*

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

### The Rejections

Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Peterson et al. (U.S. 6,674,159 B1) in view of Admitted Prior Art (hereinafter APA).

Peterson teaches the above outlined features except for herein the chip furthermore comprises at least a fuse line buried within the chip and a fuse window having an upper surface below the active surface of the chip for decreasing the thickness of the structure above the fuse line such that the passivation layer fills the fuse window. However, APA discloses a semiconductor structure with (6) wherein the chip furthermore comprises at least a fuse line buried (112) within the chip and a fuse window (114) having an upper surface below the active surface of the chip for decreasing the thickness of the structure above the fuse line (112) such that the passivation layer (110a) fills the fuse window (114) (see paragraph [0008]. Figure 1C). Thus, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Peterson (accordance with the teaching of APA). Doing so would facilitate the manufacture of the semiconductor package and increase its speed.

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chuong A. Luu whose telephone number is (571) 272-1902. The examiner can normally be reached on M-F (6:15-2:45).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David C. Nelms can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Chuong Anh Luu  
Patent Examiner  
May 13, 2005